

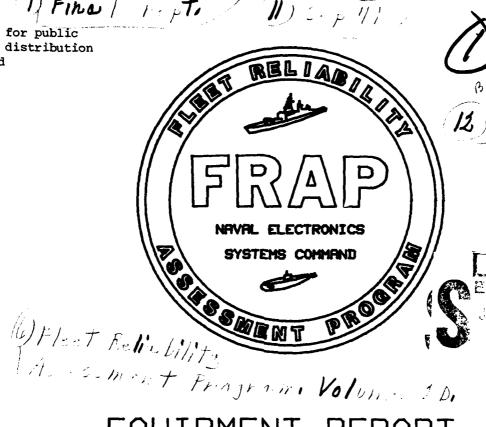


VOLUME 2D

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EQUIPMENT REPORT ON-143(V)5/USQ.

NAVAL WEAPONS SUPPORT CENTER CRANE, INDIANA

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<sup>\*</sup> Zero in this column indicates an original page.

# DEPARTMENT OF THE NAVY NAVAL ELECTRONICS SYSTEMS COMMAND

PREPARED UNDER THE DIRECTION OF

RELIABILITY ENGINEERING BRANCH

REVIEWED BY

SYSTEMS EFFECTIVENESS DIVISION

CAPTAIN, USN
DEP CDR LOGISTICS DIRECTORATE

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# VOLUME 2D ON-143(V) 5/USO

# EQUIPMENT REPORT

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VOLUME 2D ON-143(V) 5/USO EDUIPMENT PEPORT

SECTION I - RESULTS

#### 1-1 PESULTS SUMMARY

Between July 1978 and June 1979 FRAP collected data on the ON-143(V)5/USA Interconnecting Group systems installed aboard 12 Fleet ships. A total of 16,173 operating hours were accumulated during which I failure was reported. This figure places the observed equipment MTBF at 16,173 hours. At the 90% confidence level, the true MTBF is not greater than 153,502 or less than 415% hours. While no formal specifications exist for the system, it is the judgement of FRAP that the OM-143(V)5/USA meets or exceeds minimum acceptable reliability requirements. See Table 1-1 for a summary of RMA results.

### 1-1.1 HARDWARE PROBLEMS

No enronic hardware reliability problem was discovered during the sampling period.

#### 1-1.2 SOFTWARE PROBLETS

No gross software problem manifested itself during the sampling period. There was, however, a firmware problem encountered which is a baud rate difference between the ON-143(V) and the VOCODER when powering up the ON-143(V) in the VOICE mode. Voice communications cannot be accomplished until the ON-143(V) stable base clock is reset to the proper baud rate.

# 1-1.3 PECOMMENDATIONS

To effect a permanent solution to the firmware problem, it is recommended that the Submarine Satellite Information Exchange System (SSIXS) operating program be re-programmed to cause the stable base clock to initialize at the correct band rate for voice communications.

SECTION II - ON-143(V) SYSTEM DESCRIPTION

## 2-1 GENERAL

The ON-143(V) 5/USQ Interconnecting Group (IG), shown in Figure 2-1, is an electronic interface and control device within the AN/USQ-64(V)3 Communications Systems Control Central. The IG interfaces various equipments and provides sequence control for the Input/Output (I/O) devices, crypto, voice digitizer (AN/CV-3333/U), and the AN/WSC-3 Transceiver. The IG contains a microprocessor Central Processing Unit (CPU) with a Read Only Memory (ROM) that contains the Submarine Satellite Information Exchange System (SSIXS) operating program. The IG and the SSIXS operating program allow for message processing and storage, system diagnostic tests, and self tests. A TTY keyboard is the orimary operator input and system control, and a TTY printer serves as the message printout device. Figure 2-2 shows a typical SSIXS SATCOM system to illustrate the IG and system relationship.

# LEGEND

- 1. OPER OPERATIONAL\*
- 2. EQUIP EQUIPMENT\*
- 3. PARTS PARTS REPLACEMENT \*

TABLE 2-4 . DATA SUMMARY	FOR ON-1	43(V)5.	
PARAMETER	OPER	EQUIP	PARTS
OPERATIONAL			
Calendar Hours	60,792	60,792	60,792
Operating Hours	16,173	16,173	16,173
Duty Cycle	Ø.266	0.266	0.266
Sample Size	12	12	12
RELIABILITY			
Number of Failures	1	1	1
Time Between Failures-Mean	16,173	16,173	16,173
Time Between Failures-Median	11,210	11,210	11,210
Distribution			
MAINTAINABILITY			
Total Repair Time	3	3	3
Number of Repairs	1	1	1
Time to Repair-Mean	3	3	3
Time to Repair-Median			
Distribution			
Total Down Time	72	72	72
Repairs (or Maint. Act.)	1	1 1	1
Down Time-Mean	72	72	72
Down Time-Median			
Distribution			
AVAILABILITY			
Inherent	0.9998	Ø.9998	0.9998
Observed-Mean			
Observed-Med 1 an			
Effective	0.9955	0.9955	0.9955

Reference Volume 1, Paragraph 3-4 NOTE: All Time Unite Are In House

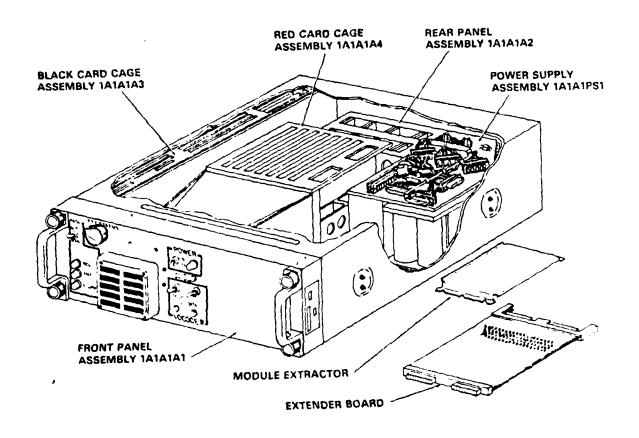


FIGURE 2-1
Interconnecting Group ON-143(V)5/USQ, Top Assembly View

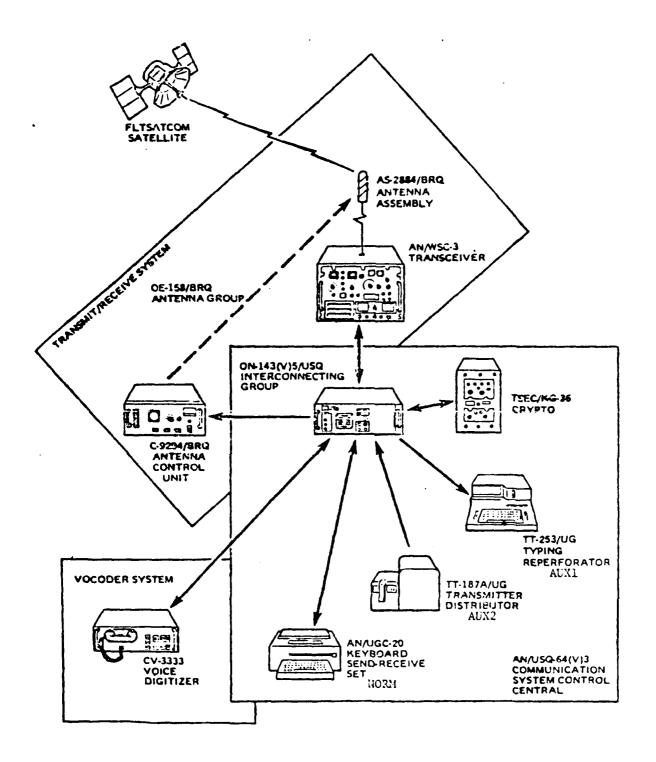


FIGURE 2-2
Interconnecting Group ON-143(V)5 and SSIXS Equipment Relationship of Units.

#### 2-2 MISSION DESCRIPTION

The IG performs six major system functions: Signal Interfacing, Gequencing of system equipments, Link Control, Message Processing, Vocoder Interface, and Monitoring for alarm conditions. The IG maintains physical and electrical isolation between Red (secure) system signals and Black (unsecure) system signals. Figures 2-3 and 2-4 illustrate the six major functions and the associated functional flow.

- a. Signal Interfacing. To accommodate variation in system equipments, the IG provides signal routing options that can be selected during installation. Signals are selectively converted with respect to time or voltage levels in order to be compatible with the rest of the system.
- b. Sequencing of System Equipments. Within the IG, circuit logic on the Red (secure) side provides for overall system control by means of a micrroprocessor Central Processing Unit and associated firmware. Under operator control, the CPU automatically generates and routes signals to properly sequence the configured system.
- c. Link Control. This function exercises the protocol established to meet the operational needs of the Fleet submarine subscribers, with regard to system mode, the relative priorities of individual subscribers, and precedence of message categories and/or voice traffic.
- d. Message Processing. The data, or message mode is the primary mode of operation for the SSIXS subscriber, however, the system can also operate in the coded voice mode. Message processing encompasses five subfunctions:
  - (1) Message Screening
  - (2) Message Storage
  - (3) Operator Interface
  - (4) Message Input
  - (5) Message Output

The subfunctions are accomplished by operator interface with the CPU and its firmware program, in conjunction with the ability of the IG to control link system equipments automatically.

- e. Vocoder Interface. The operator can switch between the data and voice modes by means of switchlights on the front panel of the IG. When in the data mode, the subscriber operator may be informed by the shore of a desire to switch over to voice by means of special request messages which are printed out on the Teletype (TTY) printer. Similarly, the subscriber can initiate the request to the shore during a subscriber initiated exchange.
- f. Monitoring and Alarm Indication. The IG performs an interface/display function in support of the system's vocoder unit. The IG also monitors the transceiver mode of operation and the crypto alarm signal.

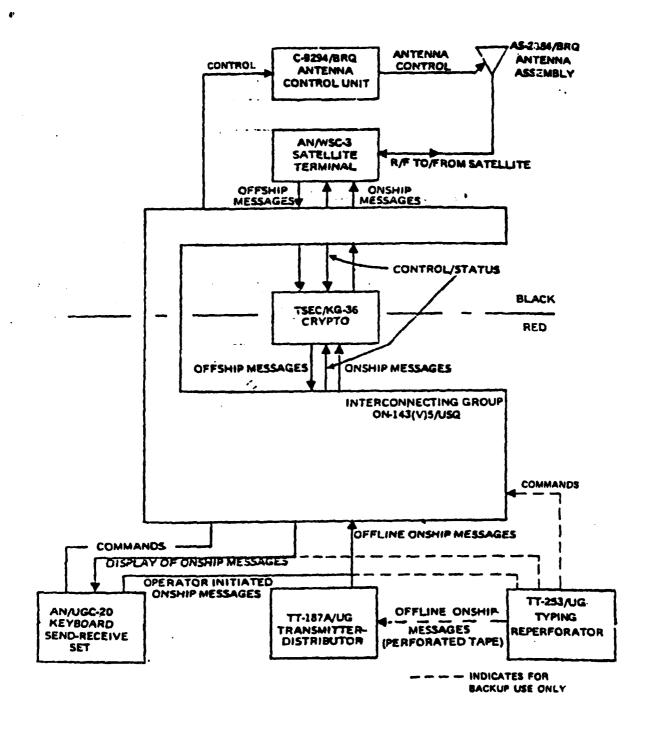


FIGURE 2-3
ON-143(V) 5 Functional Flow Diagram

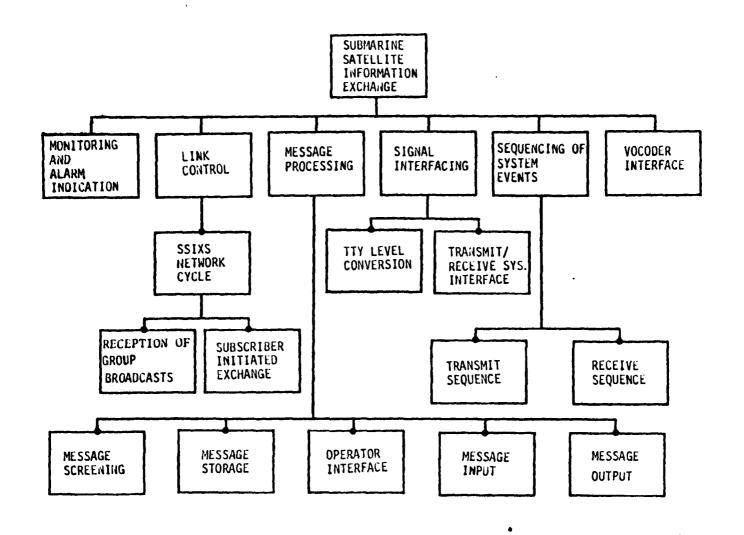


FIGURE 2-4
ON-143(V)5 System Functions

### 2-3 EQUIPMENT DESCRIPTION

System performance is the result of both hardware (electronic circuits) and firmware (processor) control and processing of a multitude of signals within the IG. Figure 2-5 is an IG Signal Function Block Diagram which depicts the signal flow of major signals among the circuit cards and to and from peripheral equipments attached to the IG.

The IG consists basically of two compartments. It processes both encrypted intelligence data and non-secure data in a compartment designated black card cage (non-secured), and processes non-encrypted intelligence data within a red card cage (secured). Circuit cards fall into three groups:

- (1) processor cards
- (2) vocoder cards
- (3) black radio cards

Figure 2-6 lists the major assemblies.

#### 2-3.1 Processor Group.

The Processor Group consists of the CPU, ROM, RAMS, I/O-1, I/O-6, and I/O-7 cards and is contained within the Red card cage. The group is a self-contained computer, less power supply, data entry and readout devices. The computer has a fixed program within the ROM card which causes it to operate as a dedicated processor. The cards communicate among each other by dedicated lines or by a common bus structure. The address selection is controlled by the CPU and is routed to all other processor cards on a common 16 line address line. Data is transferred among the cards on an eight line bi-directional data bus.

- a. Central Processing Unit (CPU). The CPU card (1A1A42) used in the ON-143(V)5/USQ, consists of an 8080A microprocessor, a 20 MHz crystal oscillator with divide by 10 circuitry, tri-state bus drivers and receivers, a status latch and decode circuit.
- b. Read Only Memory (ROM). The ROM module (1A1A38) consists of eight denot level erasable ROM chips, address recognition logic, bus timing logic, tri-state drivers/receivers, and four voltage regulators. The eight ROM chips represent 8192 8-bit words of read only storage and are used to store the SSIXS program.
- c. Random Access Memory (RAM). There are three identical RAM cards (1A1A39, 40, 41) each containing 16 dynamic RAM chips, address recognition logic, read/write timing logic, refresh circuitry for the dynamic RAMs, and tri-state drivers/receivers. The 16 dynamic RAM chips represent 8192 8-bit words of random access memory. The ON-143(V)5 can operate with only one RAM card and/or with partially defective RAM cards in a degraded mode consisting of limited message buffer storage area and/or garbled characters.
- d. Input/Output No. 1 (I/O-1). The I/O-1 circuit card (1A1A37) contains an automatic program start, an asynchronous receiver/transmitter, discrete Mil-Std-188C input/output lines, a real time clock, and an

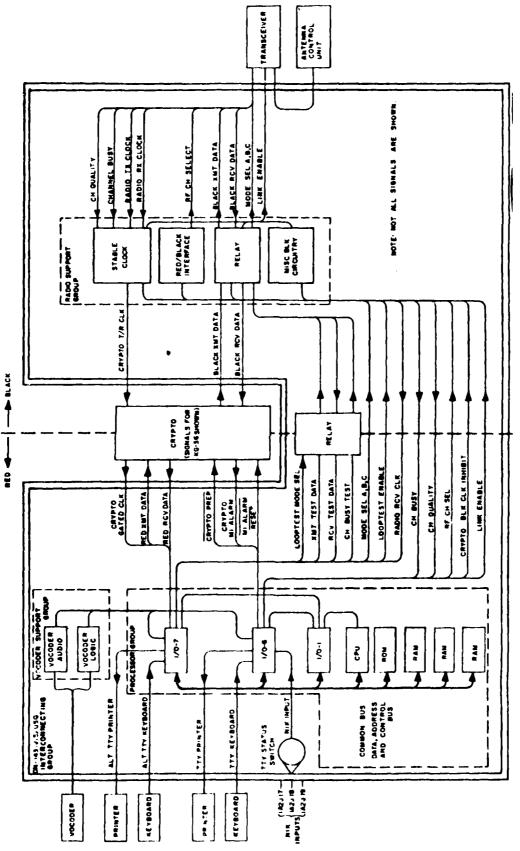


FIGURE 2-5 Signal Function Block Diagram

DI

# List of Major Assemblies

Reference	No
Designation	Name
1	INTERCONNECTING GROUP ON-143(V)5/USQ, P/N 74E2N117
1A1	INTERCONNECTING GROUP ON-143(V)5/USQ, P/N 74E2N872
1A35	CIRCUIT CARD ASSY, Discrete Data, P/N 0008374
1A36	CIRCUIT CARD ASSY, I/O Serial Data, P/N 000838 CIRCUIT CARD ASSY, I/O Basic, P/N 0008134
1A37	CIRCUIT CARD ASSY, I/O Basic, P/N 0008134
1A38	CIRCUIT CARD ASSY, Read Only Memory, P/N 0008130
139 .	CIRCUIT CARD ASSY, Random Access Memory, P/N 0008126
1A40	CIRCUIT CARD ASSY, Random Access Memory, P/N 0008126
1A41	CIRCUIT CARD ASSY, Random Access Memory, P/N 0008126
1A42	CIRCUIT CARD ASSY, Processor Control, P/N 0008122
1A1A1	FRONT PANEL ASSY, P/N 74E2N848
1A1A1A2	REAR PANEL ASSY, P/N 74E2N259-4
1414143	CARD CAGE ASSY, (Black), P/N 74E2N228
1A1A1A4	CARD CAGE ASSY, (Red), P/N 74E2N232-3
13141413	CIRCUIT CARD ASSY, Secure Voice, P/N 74E2N384
1Λ1Α1Λ14	CIRCUIT CARD ASSY, Secure Voice, P/N 74E2N388
17171719	CIRCUIT CARD ASSY, Interface Assy, P/N 74E2N900
14141417	CIRCUIT CARD ASSY, Stable Clock, P/N 74E2N356
13131318	CIRCUIT CARD ASSY, Relay, P/N 74E2N665
1A1A1A19	CIRCUIT CARD ASSY, Black Circuits, P/N 74E2N470
LVIAIPSI	POWER SUPPLY ASSY, P/N 74E2N845-2
IAIAIPSIAI	+ 12 Vdc Regulator Module
1A1A1PS1A2	Dual +5 Vdc Regulator
IAIAIPS1A3	+ 6.5 Vdc Regulator
LATATPS 1A4	+5 Vdc + 1% Regulator
1A1A1PS1A5	+130 Vdc Regulator
IMAIPS1A7	Interconnect Board

interval timer.

- e. Input/Output No. 6 (I/O-6). The I/O-6 circuit card (lAlA35) contains the control and status logic for the transmit and receive functions, level converters for TTY equipment interface, strap option headers that are wired to set data rates and interface various types of equipments, and an interrupt capability. Inputs and outputs to the common bus are via tri-state receivers/drivers.
- f. Input/Output No. 7 (I/O-7). The I/O-7 circuit card (lala36) contains serial to parallel and parallel to serial converters for the synchronous data, control for the transmit and receive clock, loopback test logic, and high level teletype level converters. The transmit and receive clocks and the serial to parallel/parallel to serial converters have interrupt capability.

### 2-3.2 Vocoder Support Group.

Two vocoder support cards provide signals in support of vocoder operation. The two cards interface with each other, the vocoder peripheral unit, and primarily the I/O-6 card. Vocoder related data and controls are applied through the I/O card to the CPU for further processing.

- a. Vocoder Logic Conversion Interface. The vocoder logic interface card (IAIAI3) allows for data or voice selection.
- b. Vocoder Audio Interface. The vocoder audio interface card (1A1A14) generates and introduces audio tones, dial tone, busy tone, and push-to-talk tone (PPT), onto the voice data signal to the vocoder.

### 2-3.3 Black Radio Support Group

The radio support group consists of the four cards in the Black Card Cage. The cards interface the radio signals to either the peripheral crypto unit or the processor group cards.

- a. Stable Base Clock. The stable base clock card (lAlAl7) provides a phase stable data clock to the receiving crypto during momentary fades or RF signal interruptions.
- b. Relay Card. The relay card (1A1A18) provides multiple signal switching under the control of the red/black isolation relays, which are controlled from the red side.
- c. Red/Black Interface Card. The red/black interface card (lAlAl6) contains relays and optical isolators to provide isolation and coupling for signals passing between the red and black compartments.
- d. Black Circuit Card. The black circuit card (lAlAl9) is an alarm circuit card which detects loss of clock, and accepts external fault indications. The card actuates an indicator lamp to indicate a malfunction of the received radio signal or other indication of improper operating mode.

#### SECTION III - SPECIFICATIONS

No formal specifications are available for the  $\mbox{ON-l43}(\mbox{V})$  Interconnecting Group.

#### SECTION JV - PROBLEMS

#### 4-1 HARDWARE

No chronic hardware problems were encountered during the sample period.

### 4-2 SOFTWARE

A Baud Rate problem has been encountered with the ON-143(V)5 system. SSIXS subscribers have been unable to establish voice mode communication initially following ON-143(V)5 power-up. The problem is the result of the ON-143(V)5 initializing itself in Voice mode at 4800 baud while the Vocoder is set to operate at 2400 baud. A temporary solution has been for the operator to reset the ON-143(V)5 stable base clock rate by switching the system to DATA mode and executing a RCV, XMT, or CLB command. A result of these commands is to cause the operating program to control the stable base clock rate in accordance with the baud rate strap options. If the strap options are set at 2400 baud for SSIXS traffic, the commands will cause the stable base clock to be reset to 2400 baud. The clock remains at 2400 baud when the system is switched back to Voice mode and therefore allows satisfactory voice mode communications. To effect a more permanent solution would require re-programming the SSIXS operating program to cause the stable base clock to initialize at 2400 baud following power-up.

# SECTION V - CORRECTIVE ACTIONS

No corrective actions were taken. However, it is recommended that action be taken to correct the voice communications baud rate problem referred to in Section I.

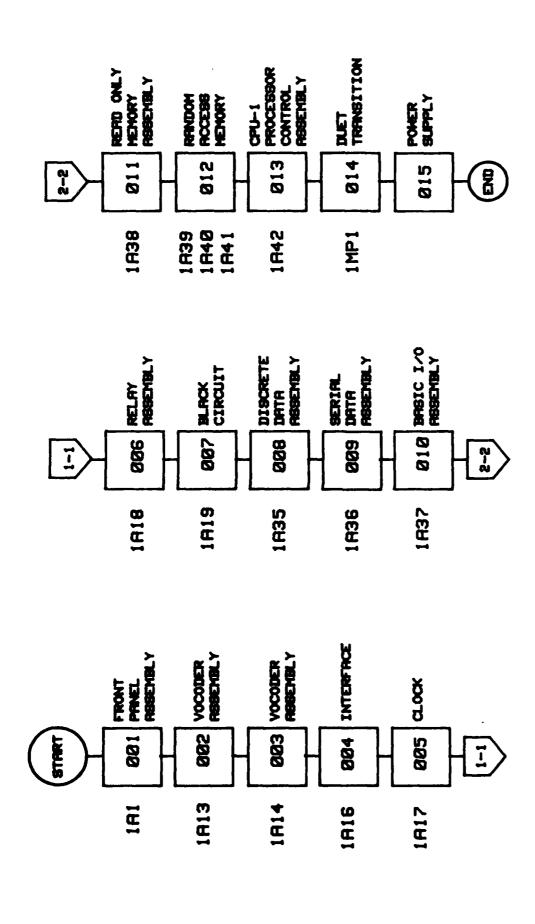
#### SECTION VI - EQUIPMENT RELIABILITY MODEL

System reliability is defined as the probability of performing a specified function or mission under specified conditions for a specified time. Reliability models are word statements or block diagrams which represent the requirements for mission success. The FRAP equipment models are used to determine the achieved operational reliability and to assess the effect of ECPs and other corrective action upon system reliability. Maintenance Action Reports are compared against the model to determine if a reported failure results in a system failure, or if not a failure, then the degree of system degradation. In addition, the model is used in determining logistic support requirements.

Maintenance of Naval shipboard equipment is accomplished by replacement or repair of components at Organizational (0), Intermediate (I), or Depot (D) repair levels. Ships Maintenance and Material Management (3-M) normally collects organizational level repair data but not intermediate or depot level repair data. Using 3-M field data requires that the

lowest components of the model be the lowest level reported by 5-M, i.e., the O-level replaceable component. This O-level component can be a piece-part, printed circuit board, major assembly, or whatever is planned for the O-level maintenance concept.

Figure 6-1 presents the ON-143(V) 5 IG (WRA 26) reliability model block diagram.



**EMRR 261** EQUIPMENT/0-Level Reliability Block Diagram for ON-143(V)5 6-1 FIGURE

## SECTION VII - ANALYSES

# 7-1 RELIABILITY

The following is the computer analysis of the observed operational reliability of the ON-143(v)5. Only the reliability analysis was performed since insufficient failures were observed to justify maintainability and availability analyses.

			<b>L</b>	F-1	ASSESSMENT	DATA					
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	Ĭ	505 505	.109	CENSORED	0	606.	.435	0	0	0	•
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3	₩90e	8244	•	INITIAL	•	•0	•	0	0	0	0
ON-143	POLLACE	825A	7.	ITIAL		•0		0	0	0	0
- T-NO	PULL ACK	8275	158.	NSORE	9	134.	. 328	0	0	0	0
5 - NO	POLL ACK	¥305	4.80°	CENSORED	456.	S	0	ø	0	0	0
Cy-143	POLL ACK	9070	779.	SS	755.	755.	.178	0	0	0	0
E - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	OUFENFISH	8157	•	Z	•	°c	0	0	0	0	0
C 7 - NO	OUF ENFISH	8307	5,49.	CENSORED	588.	ø	5	0	0	•	0
04-163 04-163	GUEENF 1SH	2006	5.88	CENSORED	588.	9	=	0	0	0	c
	CONFERENCE	9037	AA5.	CENSORED	885.	80		0	0	0	0
	٠ ا ا	9906	Œ	CENSORFO	885.	885.	.138	c	0	0	0
5 7 1 - 20	200	06 T 7	946.	_	946.	. 446	.117	0	0	0	0

			-	FLEET RELIABILITY	⋖	DATA					
SYSTEM	SHIPNAME	DATE	¥ L	FAILURE TYPE		FAILURE TIME	DUTY	ER.	٥٦)	96	0
0N-143	SEADPAGON	9180	•	INITIAL		•0	00000	0	0	0	6
0N-143	SEADPAGON	8199	156.	DEFFERED		156.	342	56	12	0	0
641-NO	SEADPAGON	8220	340	CENSORED		184	.354	0	0	0	0
0N-143	SEADPAGON	8237	427.	CENSORED		271.	.312	0	0	0	0
0N-143	SEADRAGON	8565	517.	CENSORED		361.	253	0	0	0	0
0N-143	SEADPAGON	8298	1018.	CENSORFD		862	,359	0	0	0	0
0N-143	SEADRAGON	A334	1770.	CENSORED		1614.	64.4	0	0	0	c
ON-143	SEADRAGON	8361	2225.	CENSORED		2069	515	0	0	0	
641-NO	SEADPAGON	0606	3450.	CENSORED		3294.	.523	0	0	0	0
0N-143	SEADPAGON	9107	3746.	CENSORED		3590.	.535	0	0	0	0
0N-143	SEADPAGON	9134	3843.	FIMAL		3687	505	0	0	0	0
0N-143	TINOSA	8207	0	INITIAL		ċ	00000	0	•	0	0
0N-143	TINOSA	8274	0	CENSORED		•	00000	0	0	0	0
0N-143	TINOSA	8303	201.	CENSORED		201.	780	0	0	•	
E+1-NO	T I NO SA	8337	557.	CENSORED		557.	179	0	0	0	
041-NO	TINOSA	8363	668.	CENSORED		668.	178	0	0	0	0
E 7 1 - NO	TINOSA	9058	,66a	CENSORED		899	.201	0	0	0	0
0N-143	TINOSA	6806	1157.	FINAL	1157.	1157.	195	0	•	0	0

RELIABILITY

ON-143 SYSTEM LEVEL

NO. CENSORED	1.		1.	1.	1.	1.	1.	٦.	1.	٦.	•
NO. FAILURES		-									
TIME TO FAIL	49.0	126.0	333.0	755.0	0.646	1028.0	1157.0	2528.0	2554.0	2940.0	3697.0
PEMAINING SYS. CAP.		75.									

EQUIPMENT OPERATING HOUPS (0.H.) = 16173.0

NUMBER OF FAILURES = 1. OBSERVEU FAILURE RATE/O.H. = .61831E-04

LESS THAN FOUR FAILURES THE EXPONENTIAL DISTRIBUTION IS ASSUMED

FOR THE ASSUMED DISTRIBUTION

4157.9. 90 PER CENT UCL FOR MEAN = 153502.278 1500.00 HOURS. THEREFORE THE EQUIPMENT MEETS THE SPECIFICATIONS EST. MEAN = 16173.000. EST. MFDIAN = 11210.269. 90 PER CENT LCL FOR MEAN = 90 PERCENT UCL 153502.28 IS GREATER THAN

RFLIABILITY

O-LEVEL SUMMARY

RELIAB PROBLEM	O <sub>Z</sub>
_	156.00
OBSERVED FAILURE TIMES LOW HIGH	156.00
SPEC MTBF	00.60606
UPPER 90 CONF LIM	153502.28
T EAN	16173.00
LOWER 90 CONF LIM	4157.AB
NUMBER FAILURES	
O-LEVEL NOMENCLATURE	12 HANDOM ACCESS MEMONY (PAM)
O-LEVEL BLOCK NO.	12 HANDOM
<b>₹</b>	24

